

Barrier Layer <u>22</u>
Channel Layer <u>20</u>
Substrate <u>10</u>

Figure 1A

First Dielectric <u>24</u>
Barrier Layer <u>22</u>
Channel Layer <u>20</u>
Substrate <u>10</u>

Figure 1B

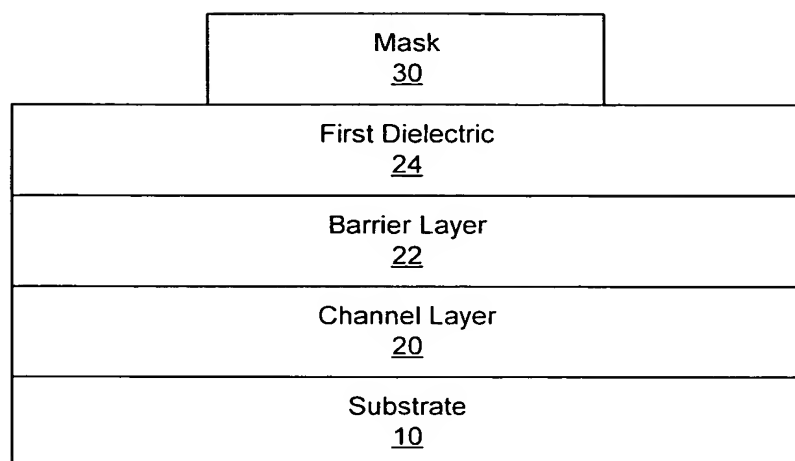


Figure 1C

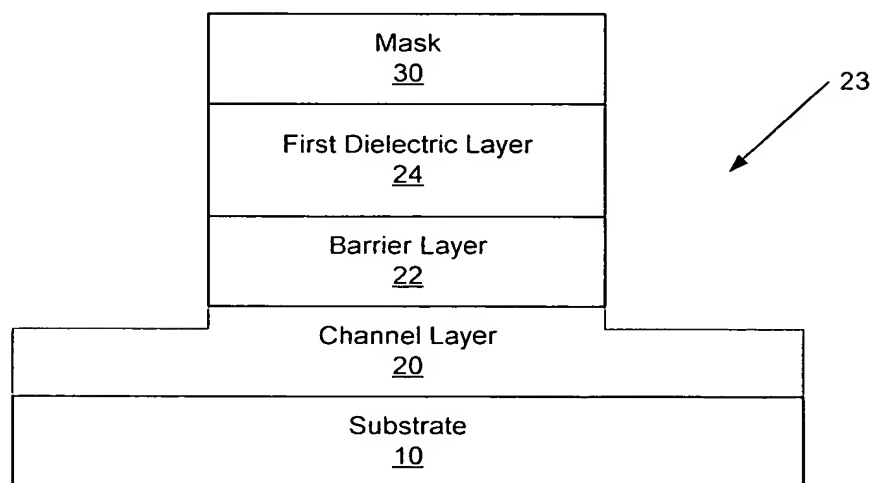


Figure 1D

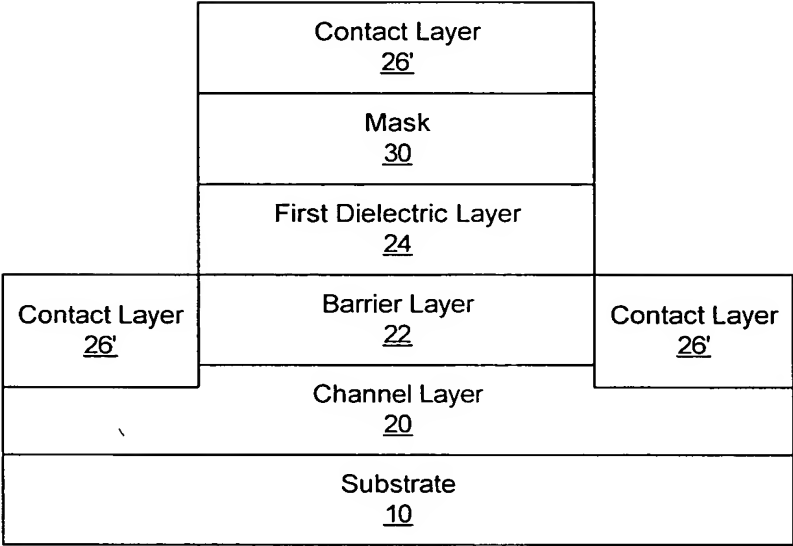


Figure 1E

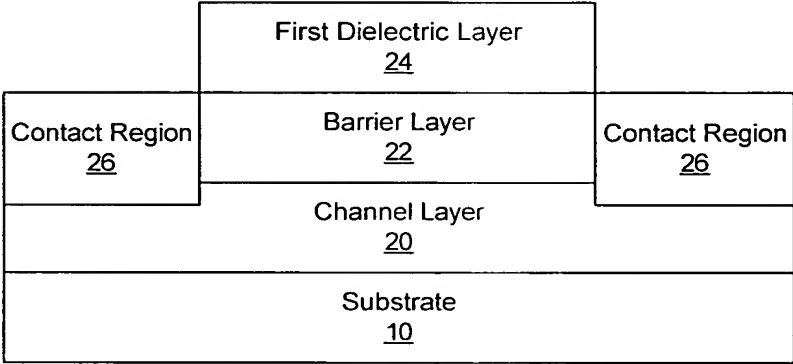


Figure 1F

<u>40</u>	<u>24</u>	<u>44</u>	<u>24</u>	<u>42</u>
Contact Region <u>26</u>	Barrier Layer <u>22</u>			Contact Region <u>26</u>
Channel Layer <u>20</u>				
Substrate <u>10</u>				

Figure 1G

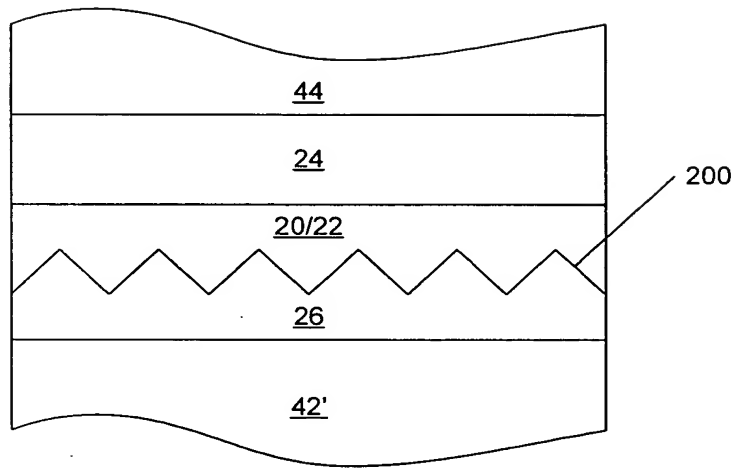


Figure 2

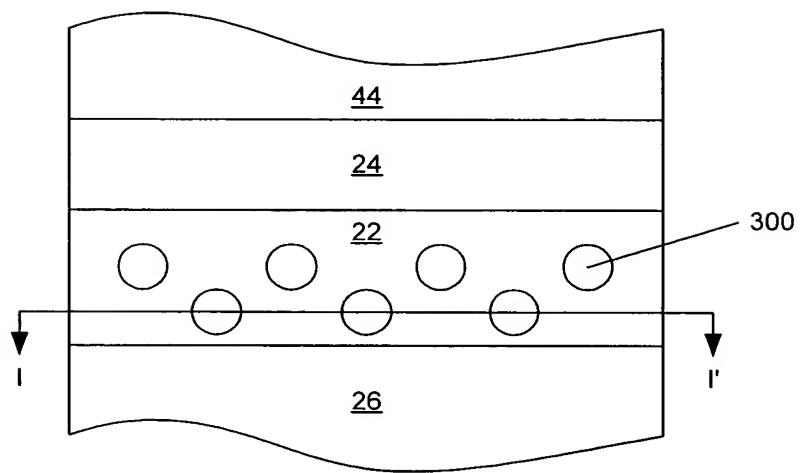


Figure 3A

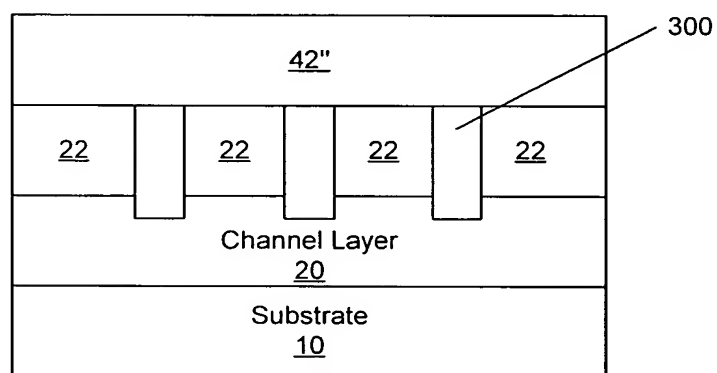


Figure 3B

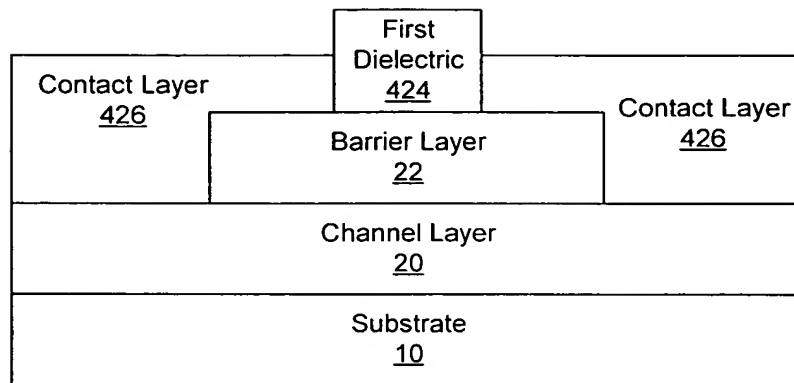


Figure 4A

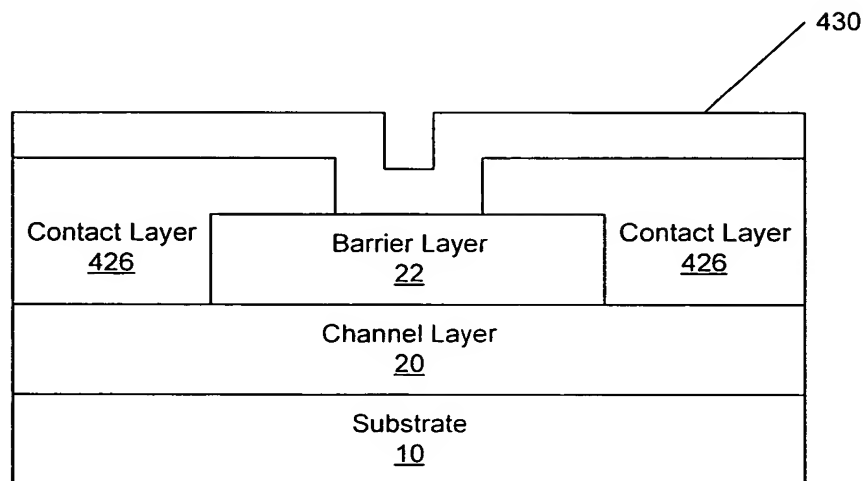


Figure 4B

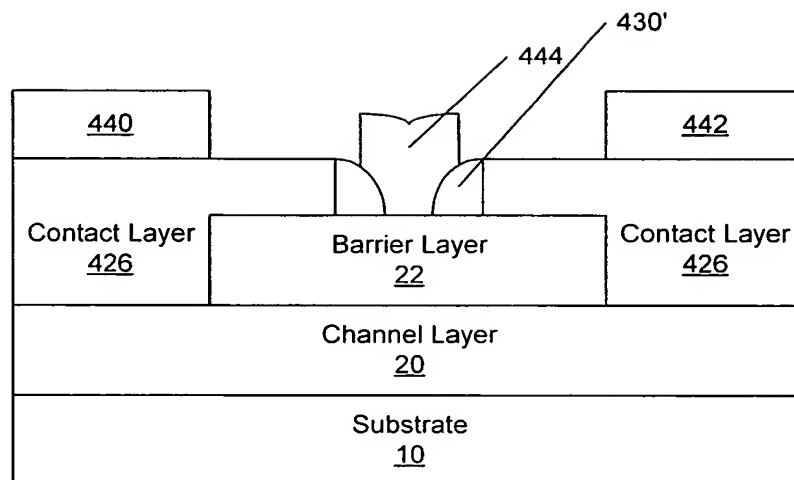


Figure 4C